

# Coriolis EDA Update

# Coriolis?

- Project from LIP6 at the Sorbonne - nearly 20 years old!
- EDA suite
- Originating from Bill SA's tools
- Python API
- Mixed signal support - combined routing for analog & digital
- Supports multiple technologies, including GF180MCU and skywater 130
- Support for IHP 130 SiGe in progress under GoIT project
- Number of propriety technos supported (out of tree) including PragmatlC flexlC

# What is Coriolis?

- A integrated EDA suite
  - Place & Route
  - LVS
  -

# Coriolis EDA Update

- New Timing aware Placer - **Coloquinte!**
- Wire-level LVS - Tremontana
- Now on wheels! - `pip install coriolis-eda`
- Moved to GitHub - <https://github.com/lip6/coriolis>
- Lots of cleaning up going on:
  - CI
  - Move to meson for build
  - Better docs (and docs in CI)
  - Support for Mac & Windows
- Join in -
  - <https://matrix.to/#/#coriolis:matrix.org>
  - <https://github.com/lip6/coriolis>